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JULY 2001
P
PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10000479	10/24/2001	257 438	392	2811 2813	H. VO

APPLICANTS: Trivedi Jigish; Violette Michael;

CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 09/055,056 04/03/1998

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FOREIGN APPLICATIONS VERIFIED:

RIGHTS DO NOT PUBLISH <input type="checkbox"/>	RIGHTS RESERVED <input type="checkbox"/>	ATTORNEY DOCKET NO
Foreign priority claimed 35 USC 1.1, Conditions met	Yes <input type="checkbox"/> No <input type="checkbox"/>	3439.4US (37-926.3)
Verifier and Acknowledged Examiner's initial		
7177.11 Interconnect structure for integrated circuit devices	Source structure for the same, and	
Instructions for fabricating the same		

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.		Figs.Drwg.
				Print Fig.
		Primary Examiner		
		PREPARED FOR ISSUE Application Examiner		
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